ABSTRACT OF THE DISCLOSURE

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A switching section is provided between a personal computer, a gateway card, and a common HDD. A CPU controls the switching section so as to connect the personal computer and the common HDD when the personal computer is in a normal power mode. The CPU controls the switching section so as to connecting the gateway card and the common HDD when the personal computer shifts to a power-saving mode. A cache memory caches data communicated between the gateway card and the personal computer, and executes in parallel the data communication and an access to the common HDD.